

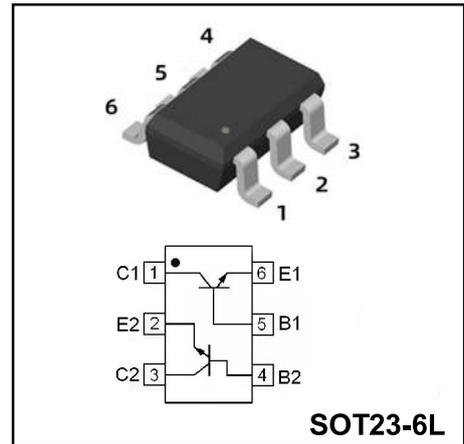
Plastic-Encapsulate Transistors

Dual NPN transistor

for preamplifier input applications

Features

Two 2SC2412K chips in SOT23-6L package



Marking Code	
1MX3	X3

Absolute Maximum Rating (Ta=25°C)

Parameter	Symbols	Value	Units
Collector-base voltage	BV_{CBO}	60	V
Collector-emitter voltage	BV_{CEO}	50	V
Emitter-base voltage	BV_{EBO}	7	V
Collector current	I_C	150	mA
Collector power dissipation	P_C	300	mW
Junction temperature	T_J	150	°C
Storage temperature	T_{STG}	-55~150	°C

Electrical Characteristics (Ta=25°C)

Characteristics	Test Condition	Symbols	Min	Typ	Max	Units
Collector-base breakdown voltage	I _C = 50μA, I _E = 0	BV_{CBO}	60			V
Collector-emitter breakdown voltage	I _C = 1mA, I _B = 0	BV_{CEO}	50			V
Emitter-base breakdown voltage	I _E = 50μA, I _C = 0	BV_{EBO}	7			V
Collector -base cut-off current	V _{CB} = 60V, I _E = 0	I_{CBO}			0.1	μA
Emitter- base cut-off current	V _{EB} = 7V, I _C = 0	I_{EBO}			0.1	μA
DC current gain	V _{CE} = 6V, I _C = 1mA	h_{FE}	120		560	
Collector-emitter saturation voltage	I _C = 50mA, I _B = 5mA	V_{CE(sat)}			0.4	V
Transition frequency	V _{CE} = 12V, I _E = -2mA, f = 100MHz	f_T		180		MHz
Output capacitance	V _{CE} = 12V, I _E = 0A, f = 1MHz	C_{ob}		2	3.5	pF

Typical Characteristics

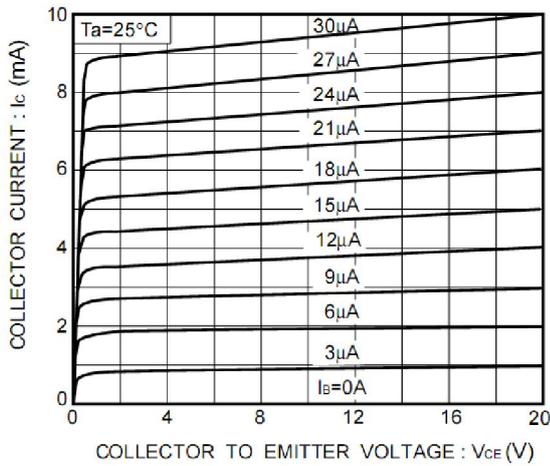


Figure 1. Static Characteristic

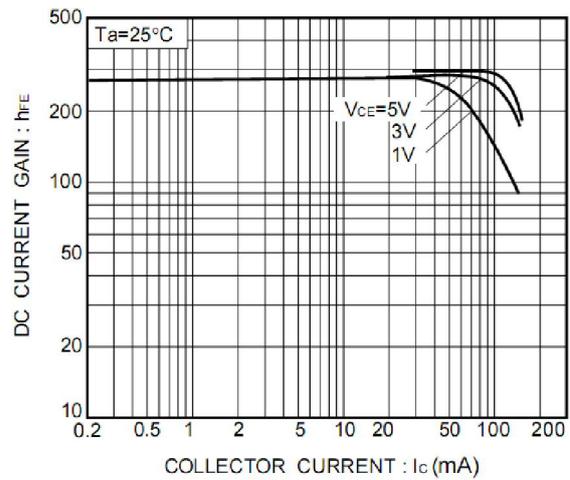


Figure 2. DC current Gain

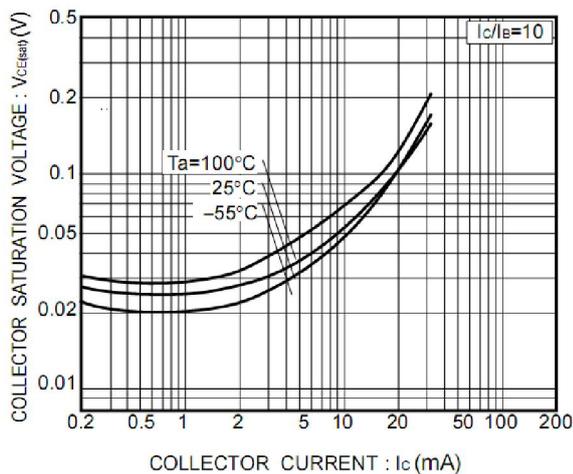


Figure 3. Collector-Emitter Saturation Voltage

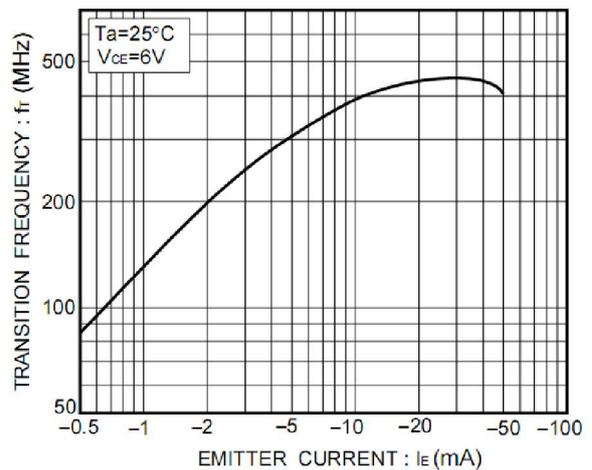


Figure 4. Gain bandwidth product

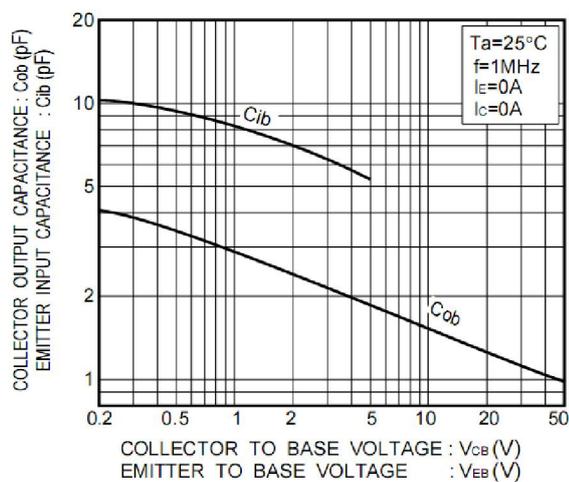


Figure 5. Capacitance

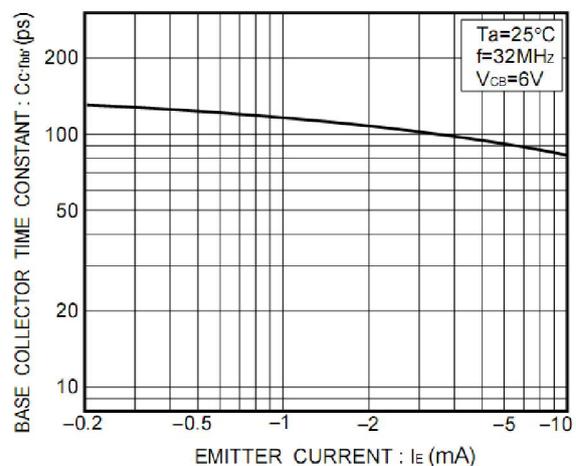


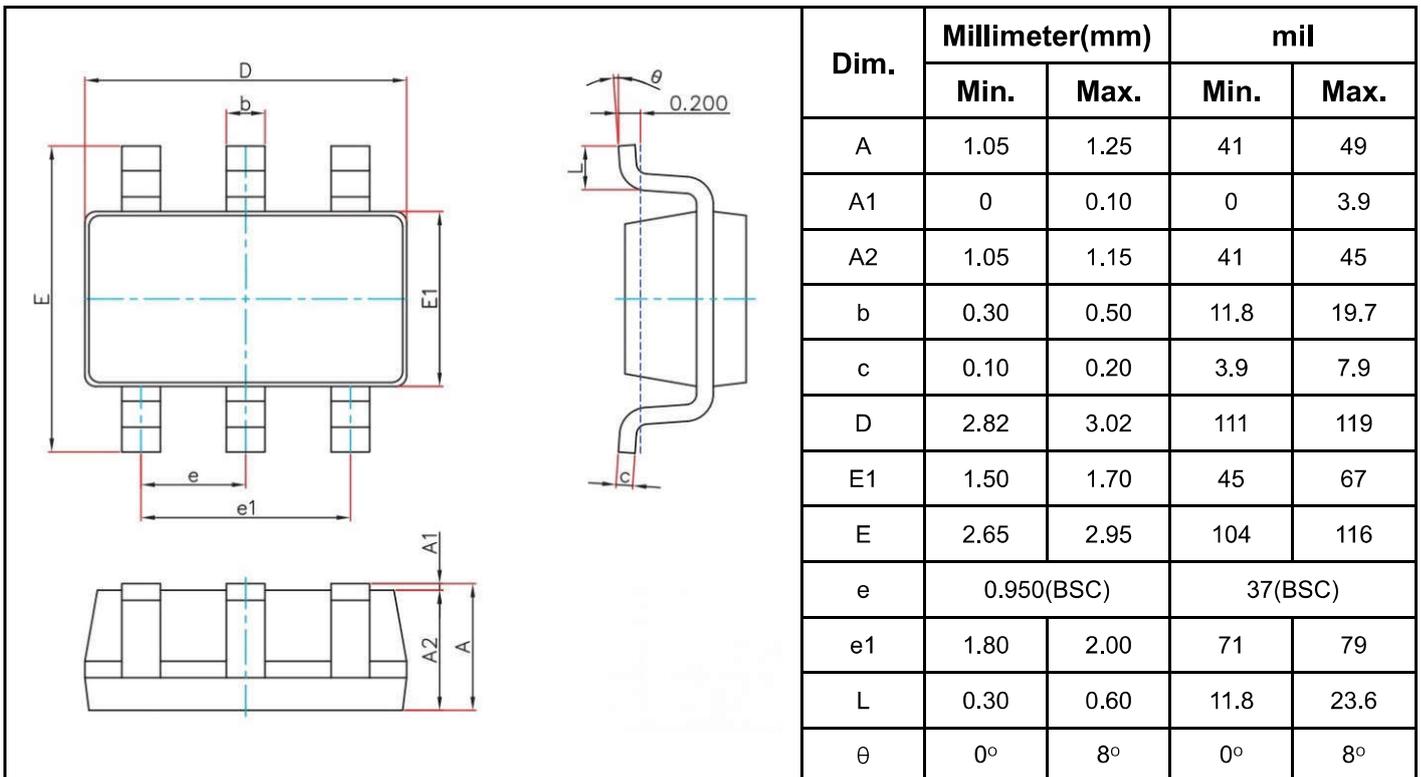
Figure 6. Base-collector time constant

Ordering information

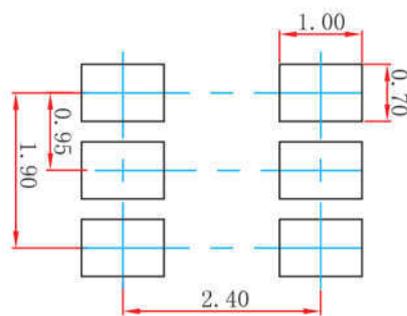
Package	Packing Description	Packing Quantity
SOT23-6L	Tape/Reel,7"reel	3000PCS/Reel 120000PCS/Carton

Package Dimensions

SOT23-6L



The recommended mounting pad size



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