

**650V Silicon Carbide (SiC) MOSFET**

**MAIN CHARACTERISTICS**

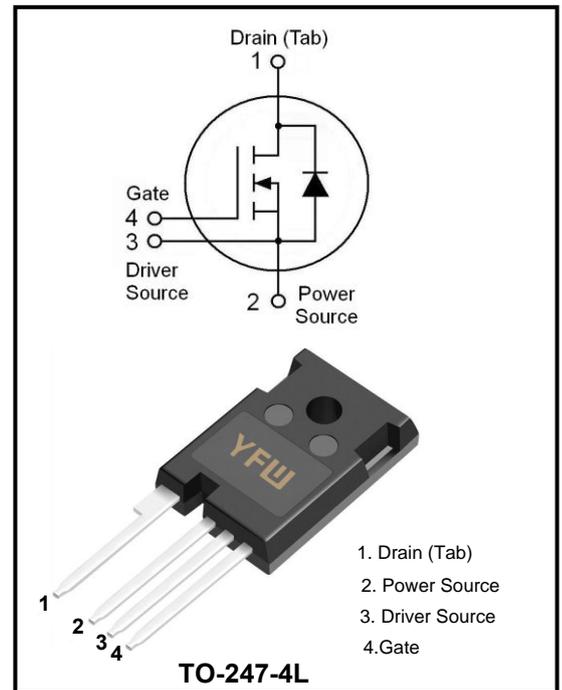
<b>I<sub>D</sub></b>	70A
<b>V<sub>DSS</sub></b>	650V
<b>R<sub>DS(on)-typ(@V<sub>GS</sub>=15V)</sub></b>	<41mΩ(Typ:30mΩ)

**FEATURES**

- ◆3rd generation SiC MOSFET technology
- ◆Optimized package with separate driver source pin
- ◆8mm of creepage distance between drain and source
- ◆High blocking voltage with low on-resistance
- ◆High-speed switching with low capacitances
- ◆Fast intrinsic diode with low reverse recovery (Q<sub>rr</sub>)

**APPLICATIONS**

- ◆Motor Drives
- ◆EV Charging Station
- ◆High Voltage DC/DC Converters



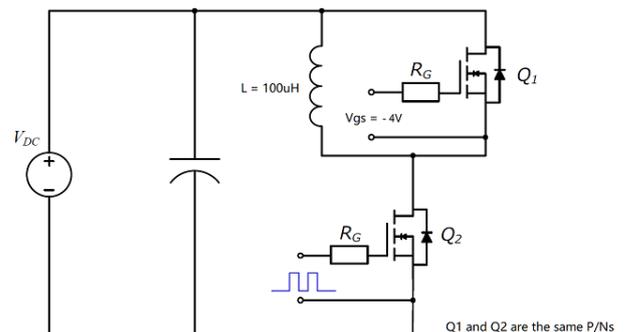
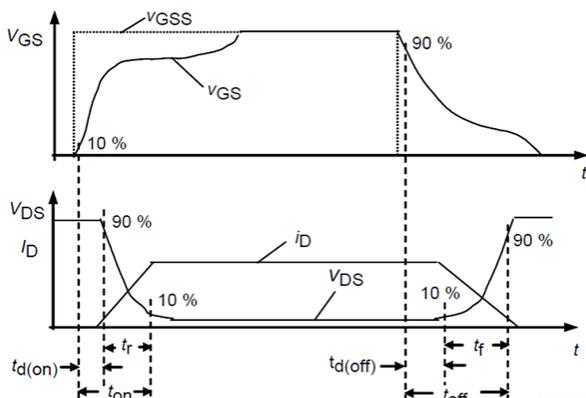
**Absolute Maximum Ratings (T<sub>c</sub> = 25°C, unless otherwise specified )**

Characteristics	Symbols	Value	Units
Drain-Source Voltage	<b>V<sub>DS</sub></b>	650	<b>V</b>
Gate-source Voltage (dynamic)	<b>V<sub>GS</sub></b>	-8/ +19	<b>V</b>
Gate source voltage (static)	<b>V<sub>GSop</sub></b>	-4 / +15	<b>V</b>
Continue Drain Current	<b>I<sub>D</sub></b>	70 49	<b>A</b>
Gate source voltage (static)	<b>I<sub>D</sub></b>	217	<b>A</b>
Avalanche Capability	<b>E<sub>AS</sub></b>	450	<b>mJ</b>
Avalanche Peak Current	<b>I<sub>AV</sub></b>	30	<b>A</b>
Power Dissipation	<b>P<sub>D</sub></b>	246	<b>W</b>
Operating Temperature Range	<b>T<sub>J</sub></b>	175	<b>°C</b>
Storage Temperature Range	<b>T<sub>STG</sub></b>	-40 to +175	<b>°C</b>
Solder Temperature	<b>T<sub>L</sub></b>	260	<b>°C</b>
Mounting Torque (M3 or 6-32 screw)	<b>M<sub>d</sub></b>	1 8.8	<b>Nm</b> <b>Lbf-in</b>
Thermal Resistance, Junction to Case	<b>R<sub>θJC</sub></b>	0.61	<b>°C/W</b>
Thermal Resistance, Junction to Ambient	<b>R<sub>θJA</sub></b>	40	<b>°C/W</b>

Note: 1. Absolute maximum ratings are those values beyond which the device could be permanently damaged. Absolute maximum ratings are stress ratings only and functional device operation is not implied.

**Electrical Characteristics at Tc=25°C unless otherwise specified**

Characteristics	Test Condition	Symbols	Min	Typ	Max	Units	
Drain-Source Breakdown Voltage	$V_{GS}=0V, I_D=100\mu A$	$BV_{DSS}$	650	-	-	V	
Drain-Source Leakage Current	$V_{DS}=650V, V_{GS}=0V$	$I_{DSS}$	-	1	50	$\mu A$	
Gate Source Leakage current	$V_{GS}=15V, V_{DS}=0V$	$I_{GSS}$		1	200	nA	
	$V_{GS}=-4V, V_{DS}=0V$	$I_{GSS}$		-1	-200	nA	
Gate-Source Threshold Voltage	$V_{DS}=V_{GS}, I_D=10mA$	$T_j=25^\circ C$	1.8	-	3.6	V	
		$T_j=150^\circ C$	-	2.0	-		
		$T_j=175^\circ C$	-	1.9	-		
Drain-source on-state resistance	$V_{GS}=15V, I_D=30A$	$T_j=25^\circ C$	-	30	41	$m\Omega$	
		$T_j=150^\circ C$	-	37	-	$m\Omega$	
		$T_j=175^\circ C$	-	41	-	$m\Omega$	
Forward Transconductance	$V_{DS}=20V, I_D=30A$	$g_{fs}$	-	23	-	S	
Input Capacitance	$V_{DS}=400V, V_{GS}=0V, f=1MHz$	$C_{iss}$	-	2550	-	$\mu F$	
Output Capacitance		$C_{oss}$	-	215	-		
Reverse Transfer Capacitance		$C_{rss}$	-	6	-		
Coss stored energy		$E_{oss}$	-	21	-		$\mu J$
Turn-on switching energy		$E_{on}$	-	110	-		$\mu J$
Turn off switching energy	$E_{off}$	-	15	-	$\mu J$		
Turn-on Delay Time	$V_{DS}=400V, I_D=30A, V_{GS}=-4V/15V, R_{G(ext)}=2\Omega, L=200\mu H$	$t_{d(on)}$	-	14	-	ns	
Rise Time		$T_r$	-	17	-		
Turn-Off Delay Time		$t_{d(OFF)}$	-	27	-		
Fall Time		$t_f$	-	7	-		
Total Gate Charge		$Q_g$	-	111	-		nC
Gate to Source Charge	$Q_{gs}$	-	32	-			
Gate to Source Charge	$Q_{gd}$	-	41	-			
Diode forward voltage	$V_{GS}=-4V, I_{SD}=15A, T_j=25^\circ C$	$V_{SD}$	-	4.5	-	V	
	$V_{GS}=-4V, I_{SD}=30A, T_j=150^\circ C$		-	4.7	-		
Diode forward current	$V_{GS}=-4V$	$I_s$	-	-	41	A	
Reverse recovery time	$V_{GS}=-4V, I_{SD}=30A, V_R=400V, di/dt=3300A/\mu s$	$t_{rr}$	-	15	-	nS	
Reverse recovery charge		$Q_{rr}$	-	198	-	$\mu C$	
Peak reverse recovery current		$I_{rrm}$	-	23	-	A	

**Switching Times Definition and Test Circuit**


Typical Characteristics

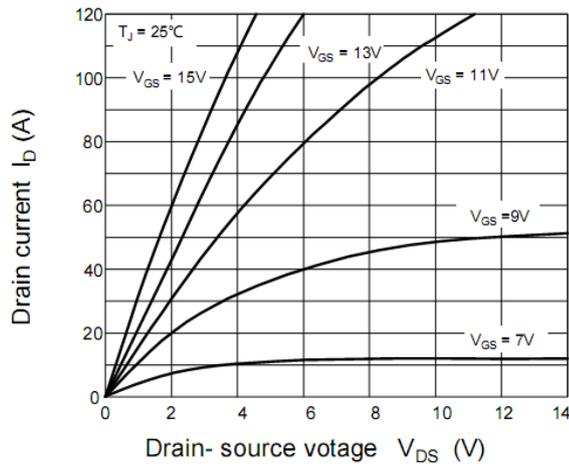


Figure 1. On Region Characteristics

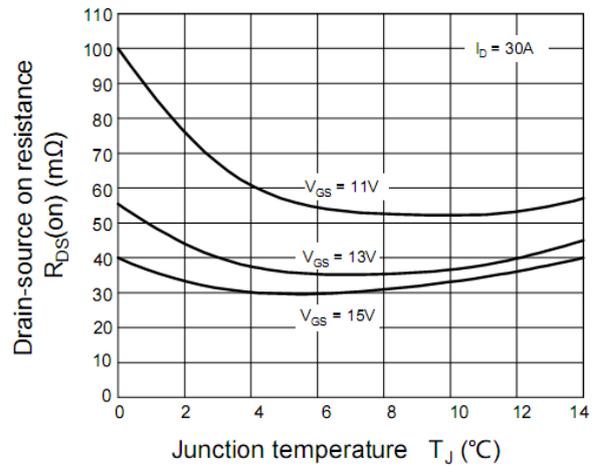


Figure 2. On-Resistance vs. Temperature

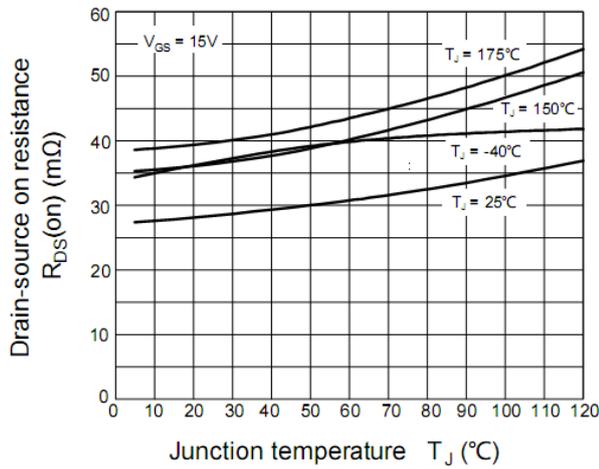


Figure 3. On-Resistance vs. Drain Current For Various Temperature

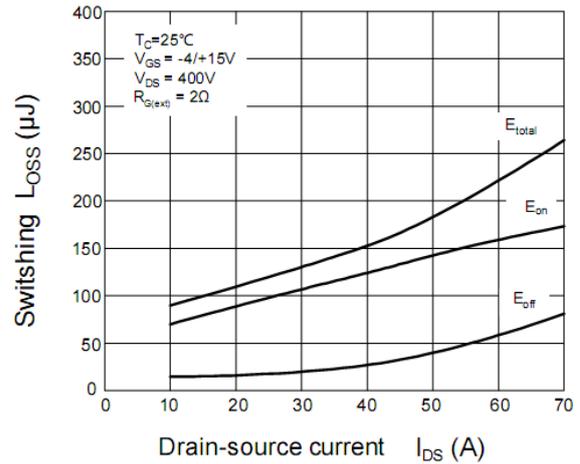


Figure 4. Switching energy vs Drain current

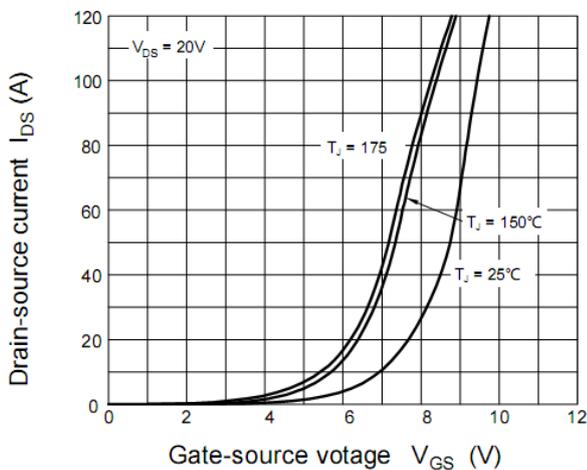


Figure 5. Transfer Characteristic

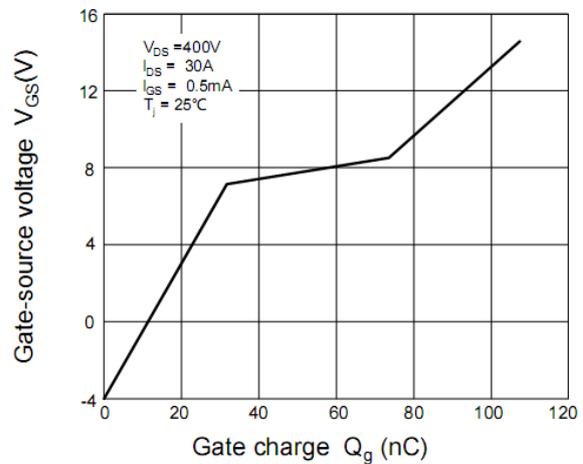


Figure 6. Gate Charge Characteristics

Typical Characteristics

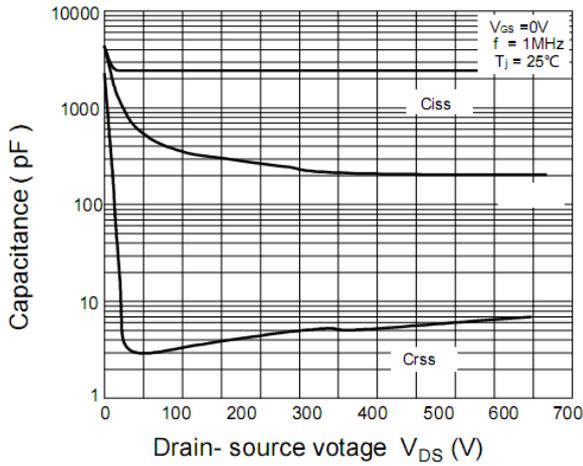


Figure 7. Capacitance vs Drain to Source Voltage

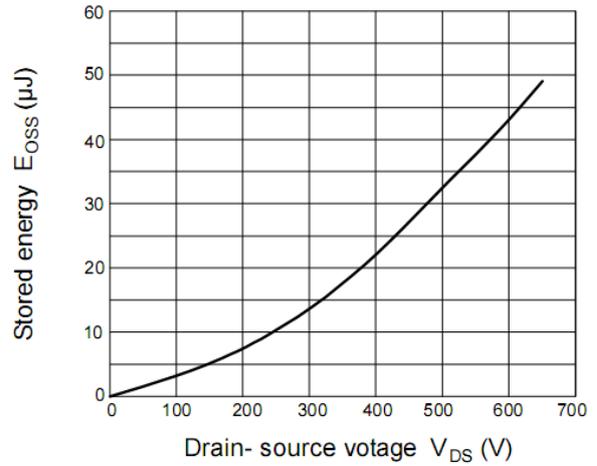


Figure 8. Output Capacitor Stored Energy

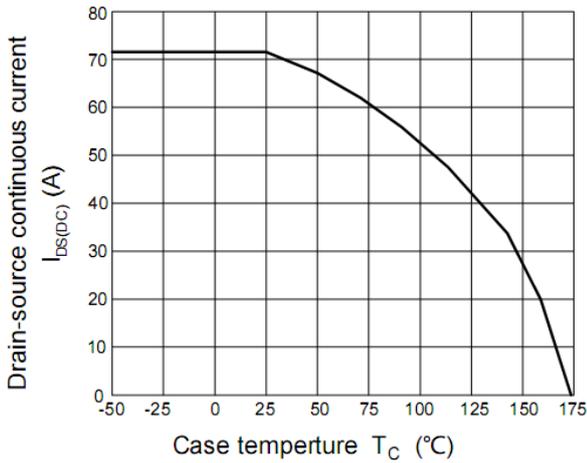


Figure 9. Continuous Drain Current vs Case Temperature

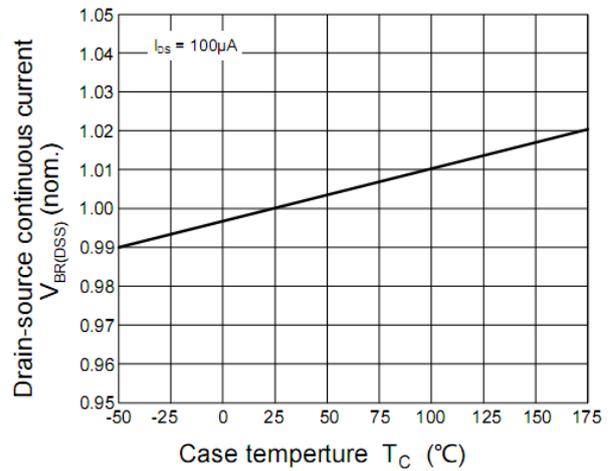


Figure 10. Normalized breakdown voltage vs Temperature

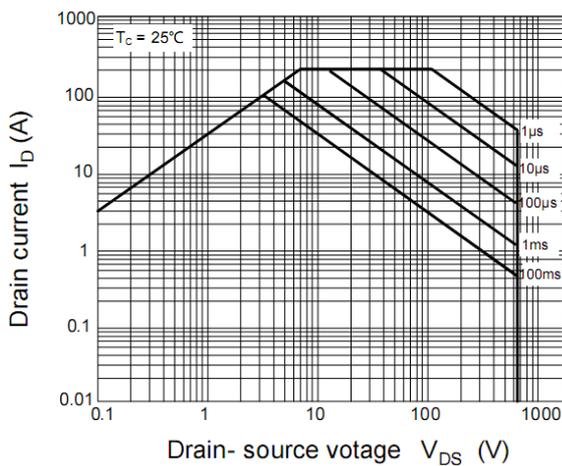


Figure 11. Safe Operating Area

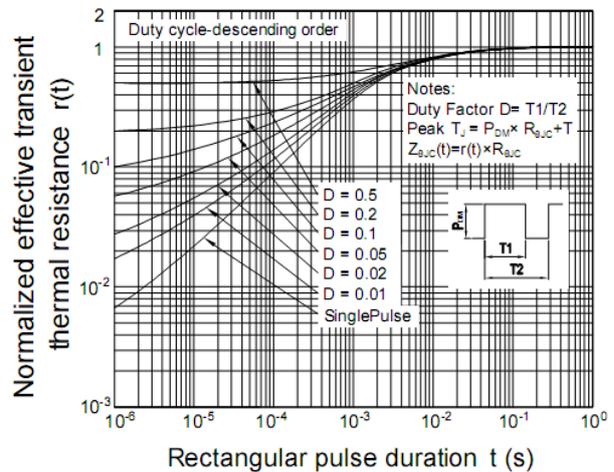
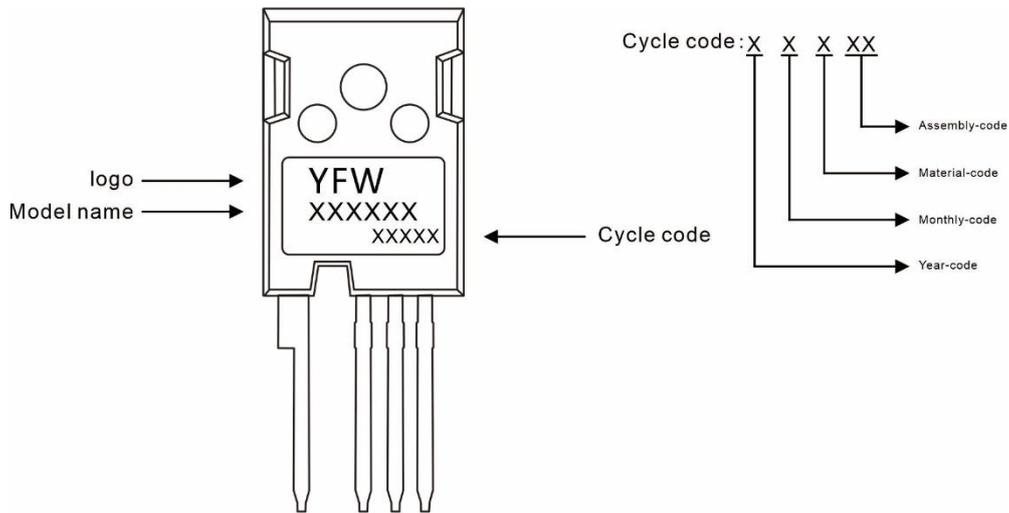


Figure 10. Junction- case Transient Thermal Response Curve

**Marking Diagram**



**Ordering information**

Model name	Package	Unit Weight	Base Quantity	Packing Quantity
YFWM370065PT	TO-247-4L	0.242oz(6.85g)	30pcs/tube	600PCS/Box 2400PCS/Carton

**Package Dimensions**

**TO-247-4L**

Symbol	Dimensions in mm		Dimensions in Inch	
	Min.	Max.	Min.	Max.
A	4.90	5.10	0.193	0.201
A1	1.90	2.10	0.075	0.083
A2	2.29	2.54	0.090	0.100
b	1.07	1.33	0.042	0.052
b1	1.20	1.50	0.047	0.059
B2	2.40	2.80	0.094	0.110
B3	2.40	2.69	0.094	0.106
c	0.55	0.68	0.022	0.027
D	15.77	16.03	0.621	0.631
D1	11.60	12.00	0.457	0.472
D2	2.40	2.70	0.094	0.106
E	23.30	23.70	0.917	0.933
e	2.54(BSC)		0.100(BSC)	
e1	5.08(BSC)		0.200(BSC)	
F	6.05	6.25	0.238	0.246
F1	5.50	5.90	0.217	0.232
F2	9.30	9.70	0.366	0.382
L	17.20	17.60	0.677	0.693
L1	4.05	4.35	0.159	0.171
L2	2.35	2.65	0.093	0.104
Φ	3.50	3.70	0.138	0.146

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