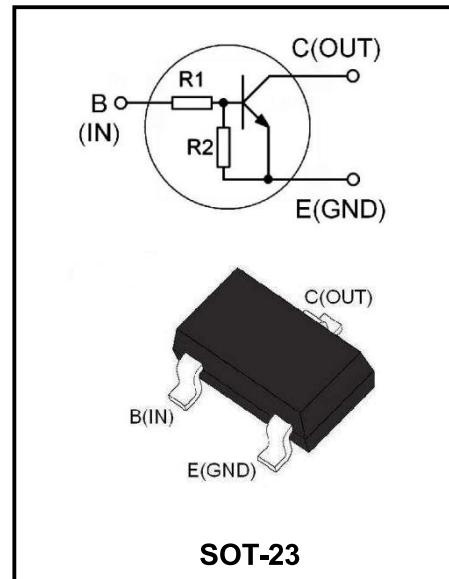


Features

- Built-In Biasing Resistors, $R_1 = 1\text{k}\Omega$, $R_2 = 10\text{k}\Omega$
- Built-in bias resistors enable the configuration of an inverter circuit without connecting external input resistors.
- Only the on/off conditions need to be set for operation, making the circuit design easy.
- Complementary PNP Types: DTB113Z



Marking Code	
DTD113Z	G21

Absolute Maximum Ratings (Ta=25°C)

Parameter	Symbol	Value	Unit
Supply voltage	V_{CC}	50	V
Input voltage	V_{IN}	-5 ~ +10	V
Output current	I_{OUT}	500	mA
Power dissipation	P_D	200	mW
Junction temperature	T_j	150	°C
Storage temperature	T_{stg}	-55~150	°C

Electrical Characteristics (Ta=25°C)

Parameter	Symbol	Conditions	Value			Unit
			Min	Typ	Max	
Input voltage	$V_{IN(off)}$	$V_{CC}=5V$, $I_o=100\mu A$			0.3	V
	$V_{IN(on)}$	$V_o=0.3V$, $I_o=20 mA$	1.5			V
Output voltage	$V_{OUT(on)}$	$I_{OUT} = 50mA$, $I_{IN} = 2.5mA$		0.1	0.3	V
Input current	I_I	$V_{IN}=5V$			7.2	mA
Output current	$I_{OUT(off)}$	$V_{CC}=50V$, $V_I=0$			0.5	μA
DC current gain	h_{FE}	$V_{OUT}=5V$, $I_{OUT}=50mA$	82			
Input resistance	R_1		0.7	1	1.3	$K\Omega$
Resistance ratio	R_2/R_1		8	10	12	
Transition frequency	f_T	$V_{CE}=10V$, $I_E=-50mA$, $f=100MHz$		200		MHz

Typical Characteristic

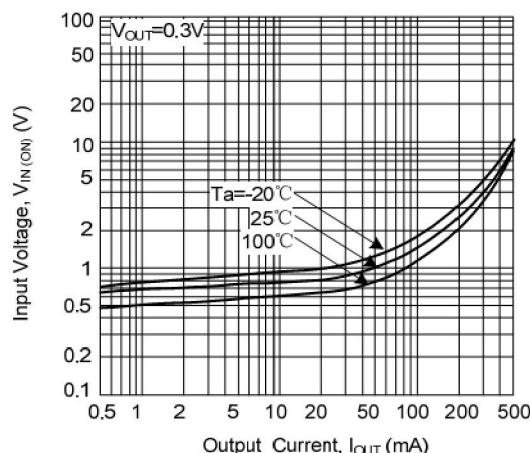


Figure 1. ON Characteristic

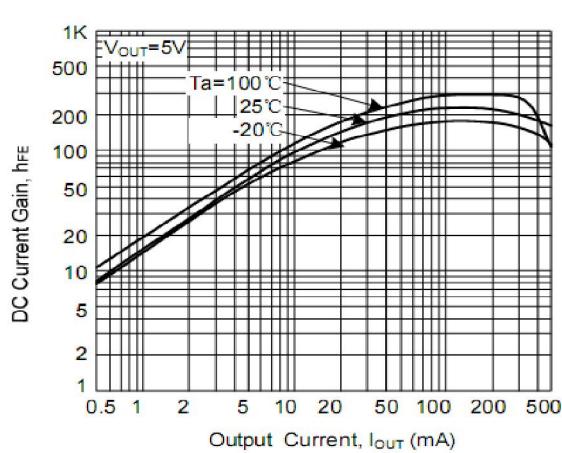


Figure 2. DC current Gain

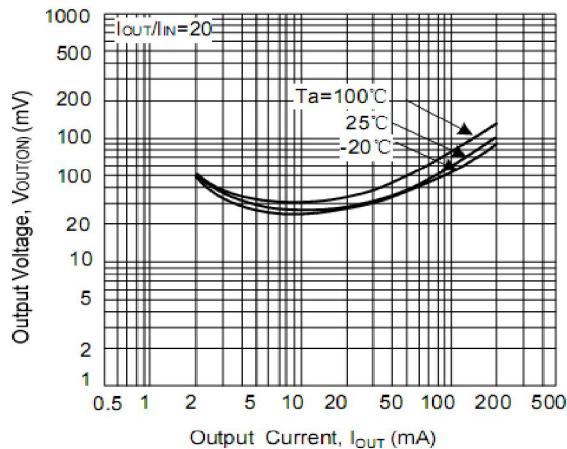


Figure 3. Output voltage vs. output current

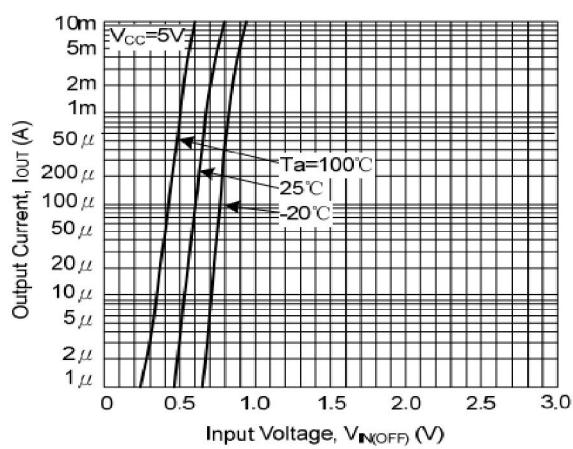
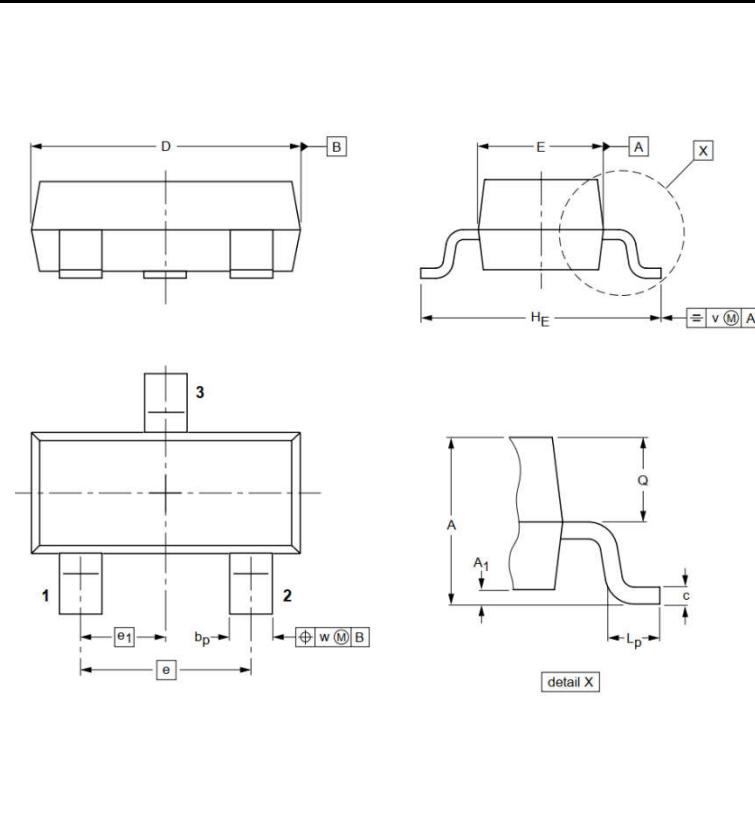


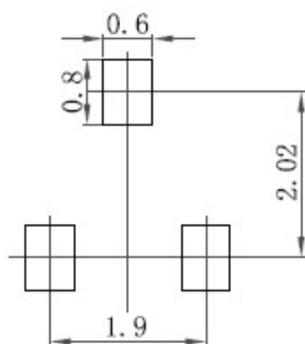
Figure 4. Output current vs. input voltage

Ordering information

Package	Packing Description	Base Quantity	Packing Quantity
SOT-23	Tape/Reel,7"reel	3000pcs/Reel	24000PCS/Box 120000PCS/Carton

Package Dimensions
SOT-23


Dim.	Millimeter (mm)		mil	
	Min.	Max.	Min.	Max.
A	0.9	1.15	35	45
A1	0.1		3.9	
bp	0.38	0.48	15	19
C	0.09	0.15	3.54	5.9
D	2.8	3.0	110	118
E	1.2	1.4	47	55
E1	0.95		37	
HE	2.1	2.55	83	100
Lp	0.15	0.45	5.9	18
Q	0.45	0.55	18	22
v	0.2		7.9	
W	0.1		4	

The recommended mounting pad size


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