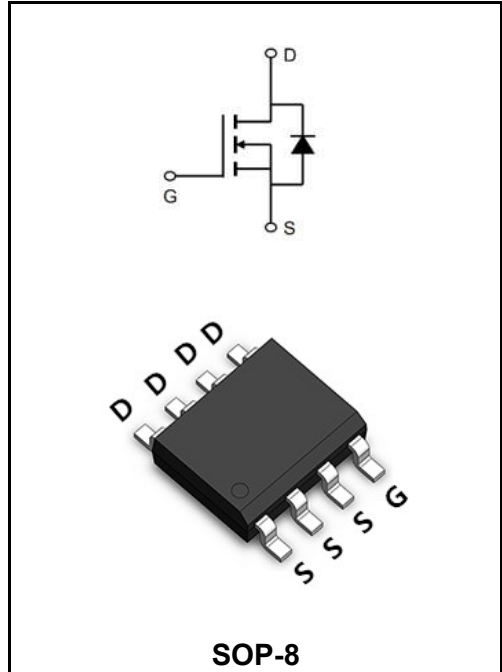


**40V N-CHANNEL ENHANCEMENT MODE MOSFET**

**MAIN CHARACTERISTICS**

<b>I<sub>D</sub></b>	10A
<b>V<sub>DSS</sub></b>	40V
<b>R<sub>DS(on)-typ(@V<sub>GS</sub>=10V)</sub></b>	< 17mΩ ( <b>Type: 14.5 mΩ</b> )



**Application**

- ◆ Battery protection
- ◆ Load switch
- ◆ Uninterruptible power supply

**Maximum Ratings at T<sub>c</sub>=25°C unless otherwise specified**

Characteristics	Symbols	Value	Units
Drain-Source Voltage	<b>V<sub>DS</sub></b>	40	<b>V</b>
Gate - Source Voltage	<b>V<sub>GS</sub></b>	±20	<b>V</b>
Continuous Drain Current <sup>1</sup> @TA=25°C	<b>I<sub>D</sub></b>	10	<b>A</b>
Continuous Drain Current <sup>1</sup> @TA=70°C	<b>I<sub>D</sub></b>	6.7	<b>A</b>
Pulsed Drain Current <sup>2</sup>	<b>I<sub>DM</sub></b>	50	<b>A</b>
Single Pulse Avalanche Energy <sup>3</sup>	<b>E<sub>AS</sub></b>	31	<b>mJ</b>
Avalanche Current	<b>I<sub>AS</sub></b>	25	<b>A</b>
Total Power Dissipation <sup>4</sup> @TA=25°C	<b>P<sub>D</sub></b>	1.9	<b>W</b>
Storage Temperature Range	<b>T<sub>STG</sub></b>	-55 to +150	<b>°C</b>
Operating Junction Temperature Range	<b>T<sub>J</sub></b>	-55 to +150	<b>°C</b>
Thermal Resistance Junction-ambient <sup>1</sup> (t≤10s)	<b>R<sub>θJA</sub></b>	40	<b>°C/W</b>
Thermal Resistance, Junction-to-Ambient <sup>1</sup>		65	<b>°C/W</b>

**Maximum Ratings at Tc=25°C unless otherwise specified**

Characteristics	Test Condition	Symbols	Min	Typ	Max	Units
Drain-Source Breakdown Voltage	$V_{GS}=0V, I_D=250\mu A$	$BV_{DSS}$	40	-	-	V
BVDSS Temperature Coefficient	Reference to 25°C, $I_D=1mA$	$\Delta BV_{DSS}/\Delta T_J$	-	0.032	-	V/°C
Static Drain-Source On-Resistance <sup>2</sup>	$V_{GS}=10V, I_D=7A$	$R_{DS(ON)}$	-	14.5	17	mΩ
	$V_{GS}=4.5V, I_D=6A$		-	18	22	
Gate -Threshold Voltage	$V_{DS}=V_{GS}, I_D=250\mu A$	$V_{GS(th)}$	1.0	-	2.5	V
VGS(th) Temperature Coefficient		$\Delta V_{GS(th)}$	-	-4.8	-	mV/°C
Drain -Source Leakage Current	$V_{DS}=32V, V_{GS}=0V, T_J=25^\circ C$	$I_{DSS}$	-	-	1	μA
	$V_{DS}=32V, V_{GS}=0V, T_J=55^\circ C$		-	-	5	
Gate-Source Leakage Current	$V_{GS}=\pm 20V, V_{DS}=0V$	$I_{GSS}$	-	-	±100	nA
Forward Transconductance	$V_{DS}=5V, I_D=7A$	$g_{FS}$	-	32	-	S
Gate Resistance	$V_{DS}=0V, V_{GS}=0V, f=1MHz$	$R_g$	-	2.1	-	Ω
Total Gate Charge(4.5V)	$V_{DS}=32V$ $V_{GS}=4.5V$ $I_D=7A$	$Q_g$	-	9.8	-	nC
Gate-Source Charge		$Q_{gs}$	-	2.8	-	
Gate-Drain Charge		$Q_{gd}$	-	3.9	-	
Turn-on delay time	$V_{DD}=20V$ $V_{GS}=10V$ $R_G=3.3$ $I_D=7A$	$t_{d(on)}$	-	2.8	-	ns
Rise Time		$T_r$	-	40.4	-	
Turn-Off Delay Time		$t_{d(OFF)}$	-	22.8	-	
Fall Time		$t_f$	-	6.4	-	
Input Capacitance	$V_{DS}=15V$ $V_{GS}=0V$ $f=1.0MHz$	$C_{iss}$	-	1013	-	pF
Output Capacitance		$C_{oss}$	-	107	-	
Reverse Transfer Capacitance		$C_{rss}$	-	76	-	
Continuous Source Current <sup>1,5</sup>	$V_G=V_D=0V, \text{Force Current}$	$I_S$	-	-	8.4	A
Pulsed Source Current <sup>2,5</sup>	$V_{GS}=0V, I_S=1A, T_J=25^\circ C$	$I_{SM}$	-	-	50	A
Diode Forward Voltage <sup>2</sup>		$V_{SD}$	-	-	1	V
Reverse Recovery Time	$I_F=7A, dI/dt=100A/\mu s,$ $T_J=25^\circ C$	$t_{rr}$	-	10	-	ns
Reverse Recovery Charge		$Q_{rr}$	-	3.3	-	nC

Note :

1.The data tested by surface mounted on a 1 inch<sup>2</sup> FR-4 board with 2OZ copper.

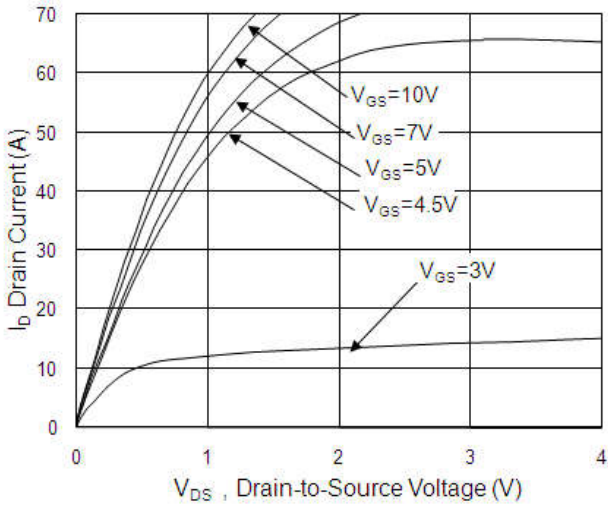
2.The data tested by pulsed , pulse width  $\leq 300\mu s$  , duty cycle  $\leq 2\%$

3.The EAS data shows Max. rating . The test condition is  $V_{DD}=25V, V_{GS}=10V, L=0.1mH, I_{AS}=25A$

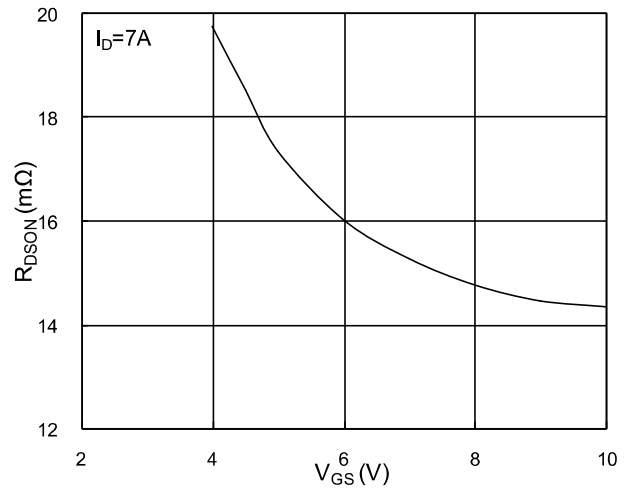
4.The po.The data is theoretically the same as Iwer dissipation is limited by 150<sub>D</sub>°C and I junction temperatureDM , in real applications , should be limited by total power dissipation.

**Ratings and Characteristic Curves**

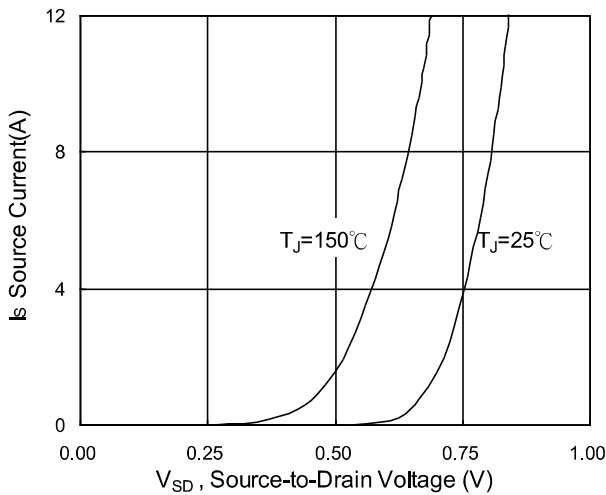
**Typical Characteristics**



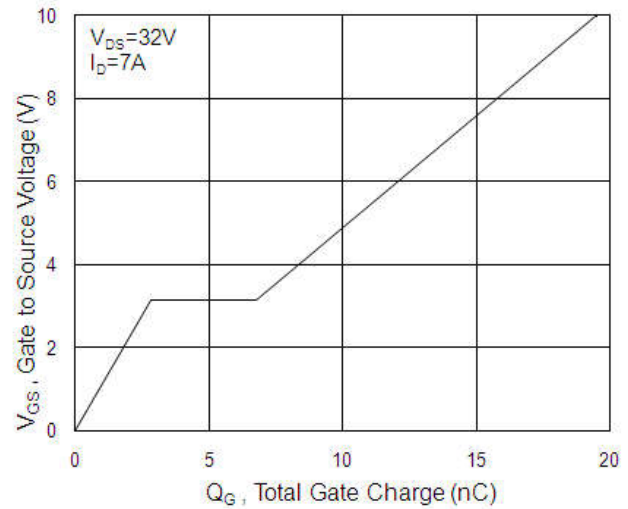
**Fig.1 Typical Output Characteristics**



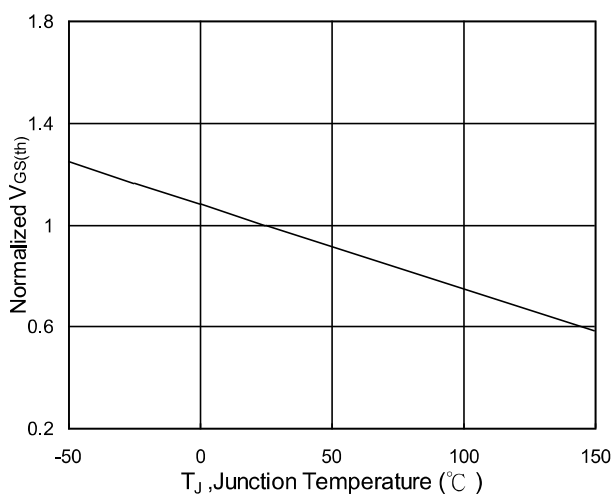
**Fig.2 On-Resistance vs. G-S Voltage**



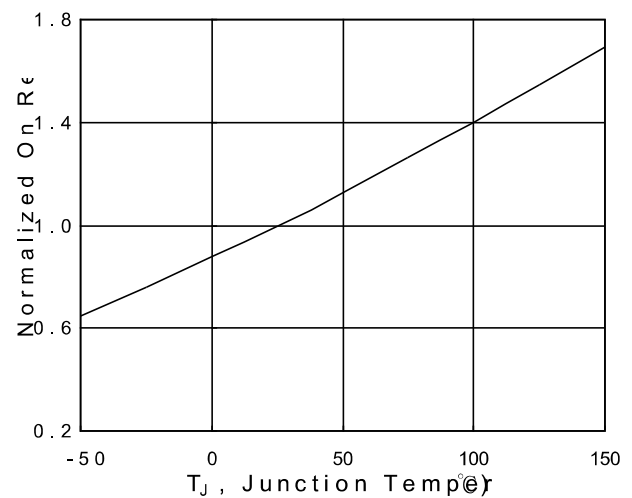
**Fig.3 Forward Characteristics of Reverse**



**Fig.4 Gate-Charge Characteristics**

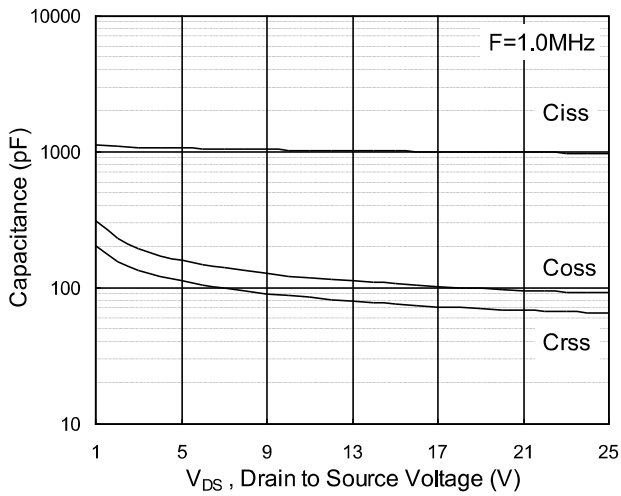


**Fig.5 Normalized  $V_{GS(th)}$  vs.  $T_J$**

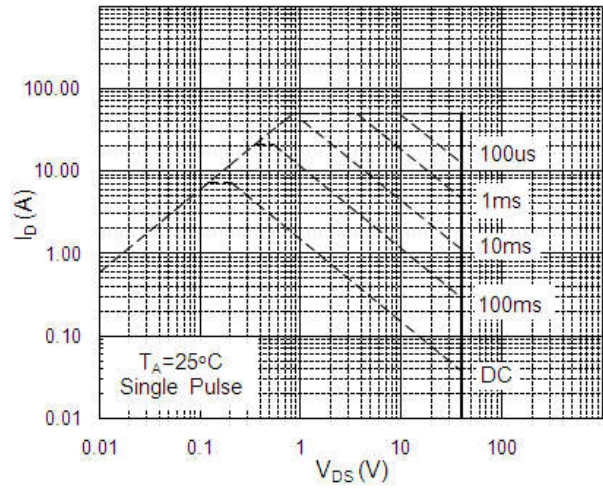


**Fig.6 Normalized  $R_{DS(on)}$  vs.  $T_J$**

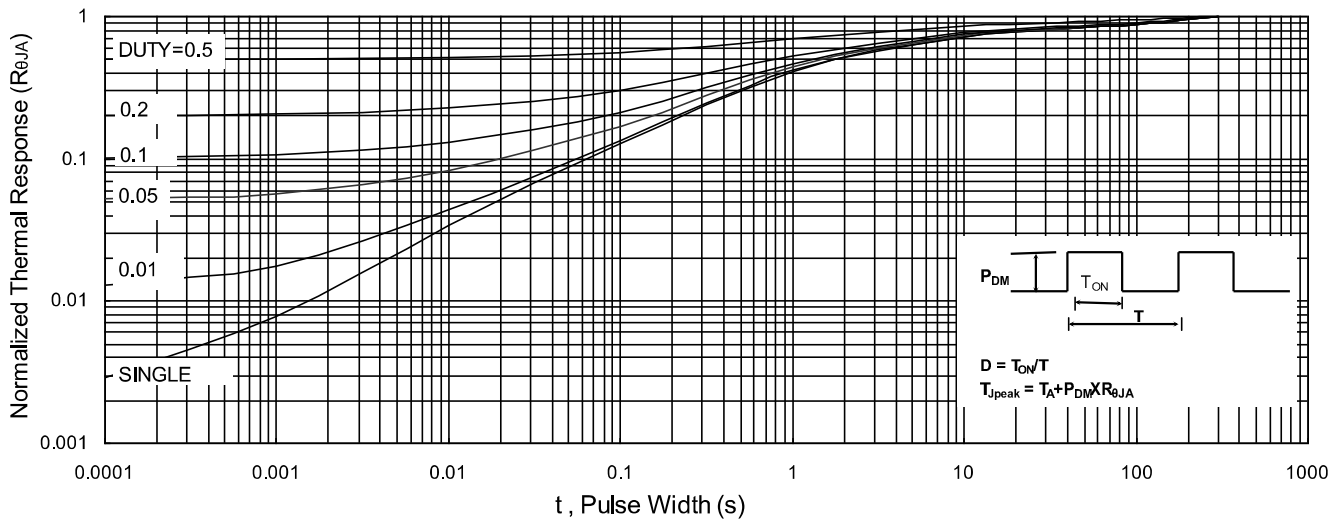
**Ratings and Characteristic Curves**



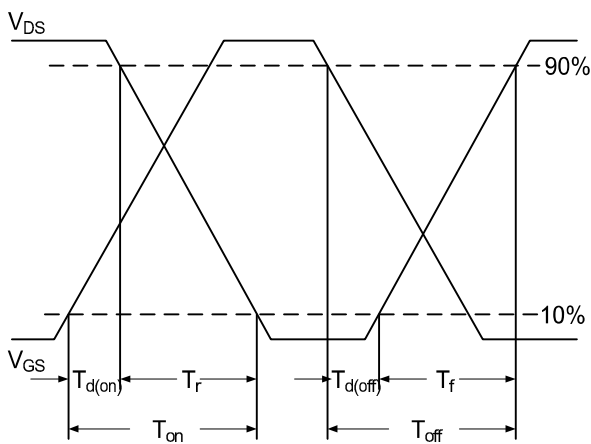
**Fig.7 Capacitance**



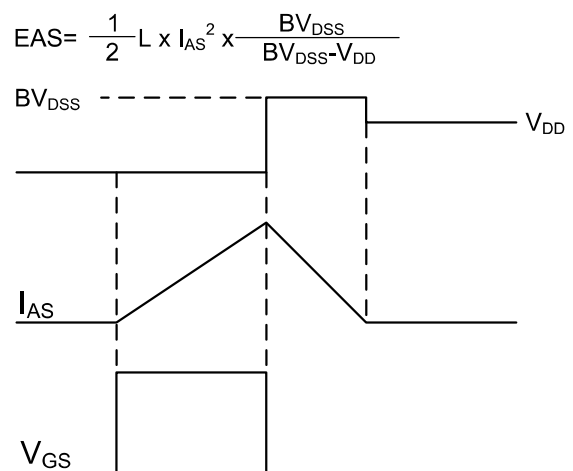
**Fig.8 Safe Operating Area**



**Fig.9 Normalized Maximum Transient Thermal Impedance**

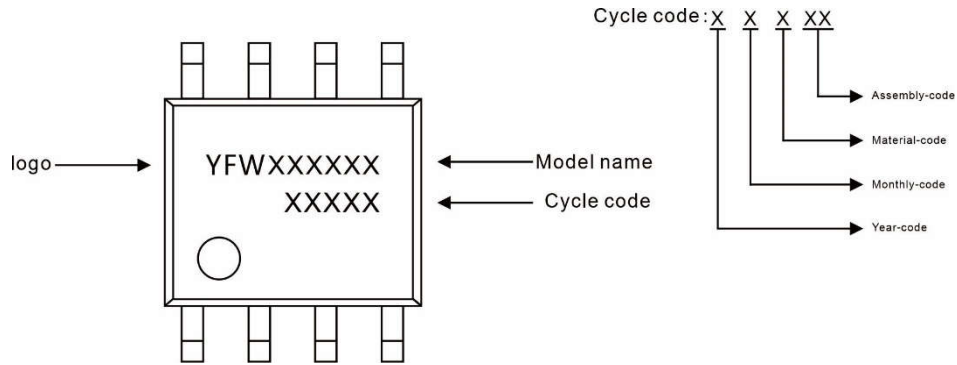


**Fig.10 Switching Time Waveform**



**Fig.11 Unclamped Inductive Switching Waveform**

**Marking Diagram**



**Ordering information**

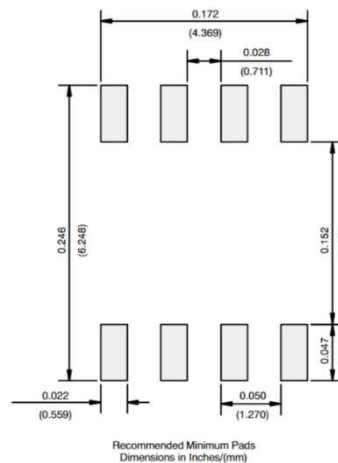
Package	Packing Description	Packing Quantity
SOP-8	Tape/Reel, 13" reel	3000PCS/Reel 30000PCS/Carton

**Package Dimensions**

**SOP-8**

Dim	Millimeter		Inches	
	Min.	Max.	Min.	Max.
A	1.35	1.75	0.053	0.069
A1	0.10	0.25	0.004	0.010
A2	1.35	1.50	0.053	0.059
b	0.35	0.55	0.014	0.022
c	0.15	0.25	0.006	0.010
D	4.80	5.00	0.189	0.197
D1	3.10	3.50	0.122	0.138
E	5.80	6.20	0.228	0.244
E1	3.80	4.00	0.150	0.157
E2	2.20	2.60	0.087	0.102
e	1.27 (BSC)		0.050 (BSC)	
L	0.40	1.27	0.016	0.050
θ	0°	8°	0°	8°

**The recommended mounting pad size**



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