

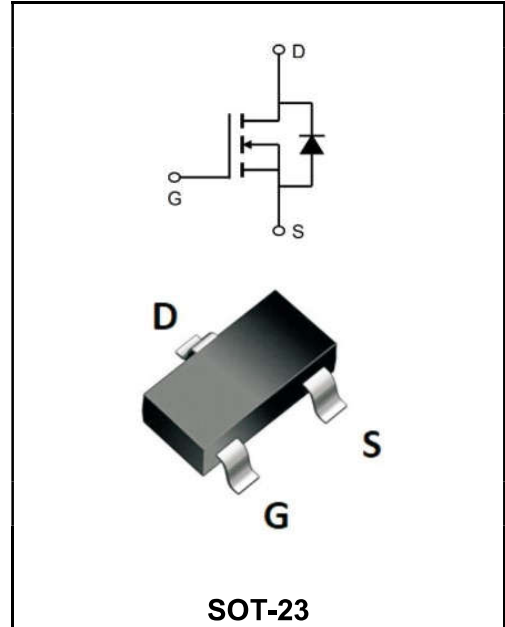
60V N-CHANNEL ENHANCEMENT MODE MOSFET

MAIN CHARACTERISTICS

I_D	3A
V_{DSS}	60V
$R_{DS(on)-typ}(@V_{GS}=10V)$	< 100mΩ (Type:80 mΩ)

Application

- ◆ Battery protection
- ◆ Load switch
- ◆ Uninterruptible power supply



Marking Code	
YFW3N06	6003

Maximum Ratings at Tc=25°C unless otherwise specified

Characteristics	Symbols	Value	Units
Drain-Source Voltage	V_{DS}	60	V
Gate - Source Voltage	V_{GS}	±20	V
Continuous Drain Current, $V_{GS} @ 10V^1 @ T_C=25^\circ C$	I_D	3.0	A
Continuous Drain Current, $V_{GS} @ 10V^1 @ T_A=70^\circ C$	I_D	1.8	A
Pulsed Drain Current ²	I_{DM}	9.2	A
Total Power Dissipation ⁴ @ $T_A=25^\circ C$	P_D	1	W
Storage Temperature Range	T_{STG}	-55 to +150	°C
Operating Junction Temperature Range	T_J	-55 to +150	°C
Thermal Resistance Junction-Ambient ¹	$R_{\theta JA}$	125	°C/W
Thermal Resistance Junction-Case ¹	$R_{\theta JC}$	80	°C/W

Maximum Ratings at Tc=25°C unless otherwise specified

Characteristics	Test Condition	Symbols	Min	Typ	Max	Units
Drain-Source Breakdown Voltage	$V_{GS}=0V, I_D=250\mu A$	BV_{DSS}	60	-	-	V
BVDSS Temperature Coefficient	Reference to 25°C, $I_D=1mA$	$\Delta BV_{DSS}/\Delta T_J$	-	0.054	-	V/°C
Static Drain-Source On-Resistance ²	$V_{GS}=10V, I_D=2A$	$R_{DS(ON)}$	-	80	100	mΩ
	$V_{GS}=4.5V, I_D=1A$		-	85	110	
Gate -Threshold Voltage	$V_{DS}=V_{GS}, I_D=250\mu A$	$V_{GS(th)}$	1.2	-	2.5	V
$V_{GS(th)}$ Temperature Coefficient		$\Delta V_{GS(th)}$	-	-4.96	-	mV/°C
Drain -Source Leakage Current	$V_{DS}=48V, V_{GS}=0V, T_J=25^\circ C$	I_{DSS}	-	-	1	μA
	$V_{DS}=48V, V_{GS}=0V, T_J=55^\circ C$		-	-	5	
Gate-Source Leakage Current	$V_{GS}=\pm 20V, V_{DS}=0V$	I_{GSS}	-	-	±100	nA
Forward Transconductance	$V_{DS}=5V, I_D=2A$	g_{FS}	-	13	-	S
Total Gate Charge(4.5V)	$V_{DS}=48V$ $V_{GS}=4.5V$ $I_D=2A$	Q_g	-	5	7.0	nC
Gate-Source Charge		Q_{gs}	-	1.68	2.4	
Gate-Drain Charge		Q_{gd}	-	1.9	2.7	
Turn-on delay time	$V_{DD}=30V$ $V_{GS}=10V$ $R_G=3.3$ $I_D=2A$	$t_{d(on)}$	-	1.6	3.2	ns
Rise Time		T_r	-	7.2	13	
Turn-Off Delay Time		$t_{d(OFF)}$	-	25	50	
Fall Time		t_f	-	14.4	28.8	
Input Capacitance	$V_{DS}=15V$ $V_{GS}=0V$ $f=1.0MHz$	C_{iss}	-	511	715	pF
Output Capacitance		C_{oss}	-	38	53	
Reverse Transfer Capacitance		C_{rss}	-	25	35	
Continuous Source Current ^{1,4}	$V_G=V_D=0V$, Force Current	I_S	-	-	2.3	A
Pulsed Source Current ^{2,4}		I_{SM}	-	-	9.2	A
Diode Forward Voltage ²	$V_{GS}=0V, I_S=1A, T_J=25^\circ C$	V_{SD}	-	-	1.2	V
Reverse Recovery Time	$I_F=2A, dI/dt=100A/\mu s,$ $T_J=25^\circ C$	t_{rr}	-	9.7	-	ns
Reverse Recovery Charge		Q_{rr}	-	5.8	-	nC

Note :

- 1.The data tested by surface mounted on a 1 inch² FR-4 board with 2OZ copper.
- 2.The data tested by pulsed , pulse width $\leq 300\mu s$, duty cycle $\leq 2\%$
- 3.The power dissipation is limited by 150°C junction temperature.
- 4.The data is theoretically the same as ID and IDM , in real applications , should be limited by total power dissipation.

Ratings and Characteristic Curves

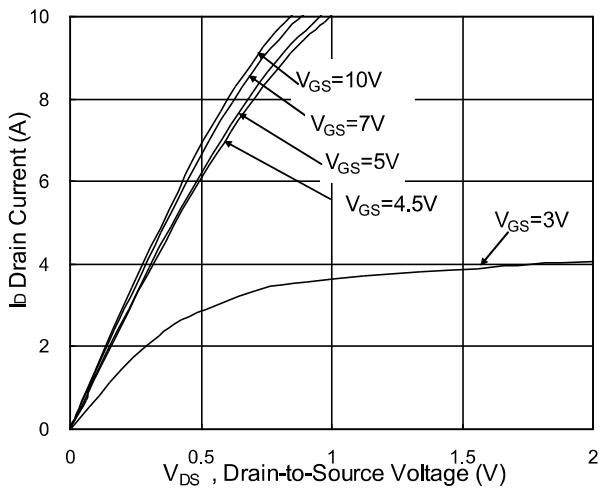


Fig.1 Typical Output Characteristics

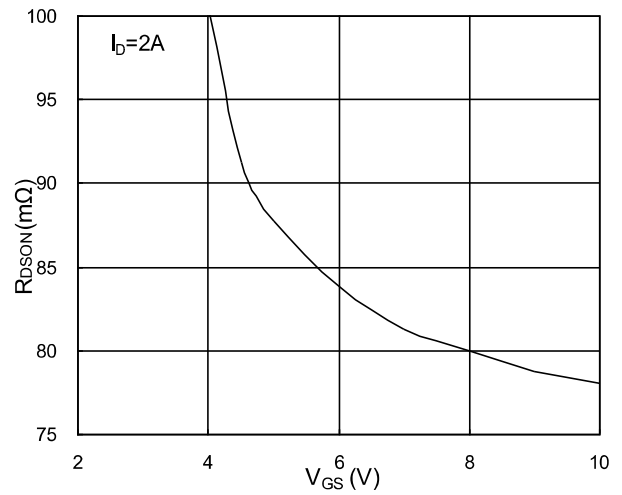


Fig.2 On-Resistance v.s Gate-Source

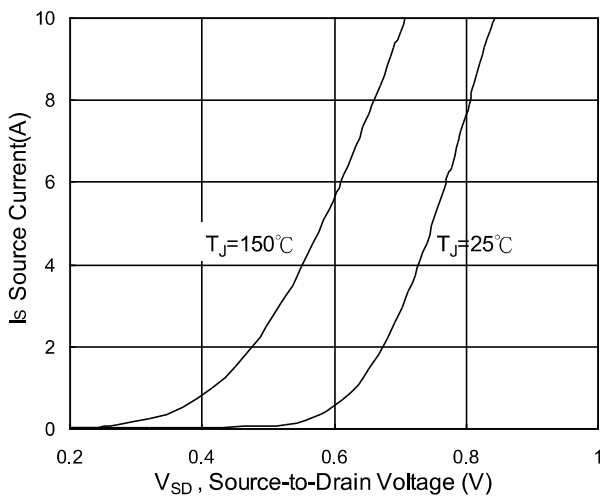


Fig.3 Forward Characteristics of Reverse

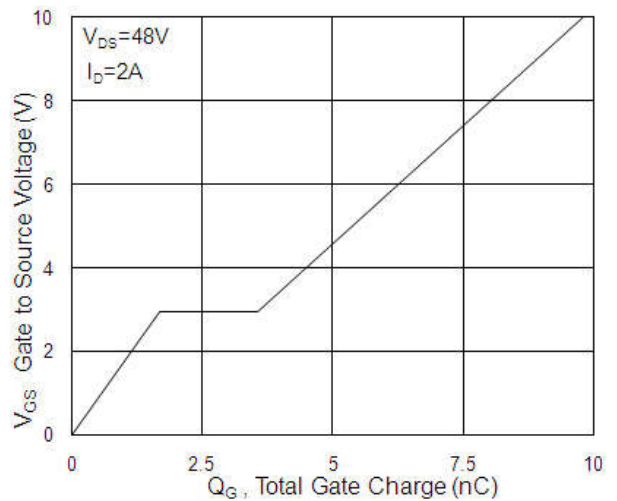


Fig.4 Gate-Charge Characteristics

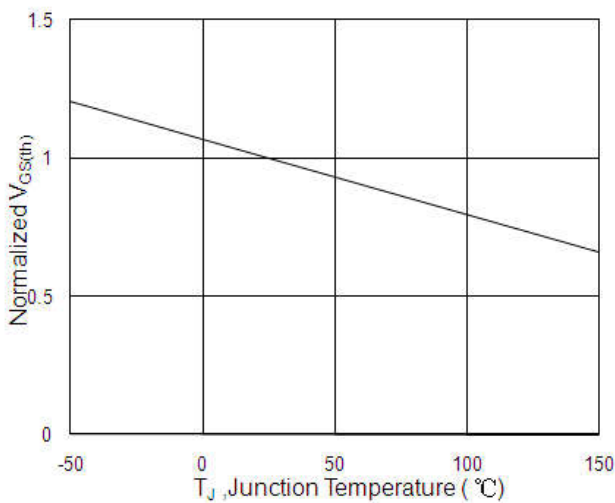


Fig.5 Normalized $V_{GS(th)}$ v.s T_J

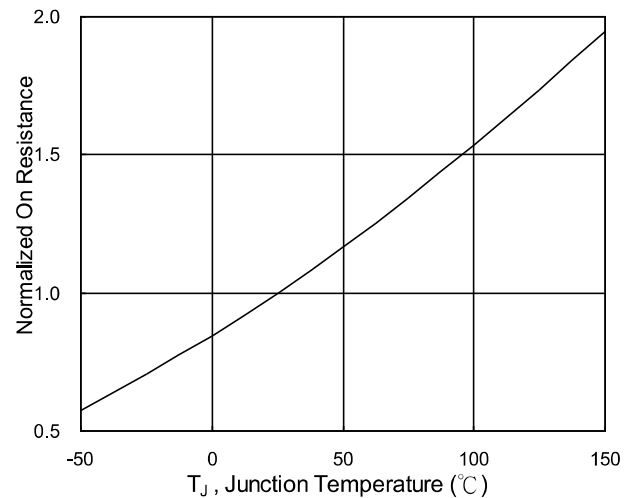


Fig.6 Normalized $R_{DS(on)}$ v.s T_J

Ratings and Characteristic Curves

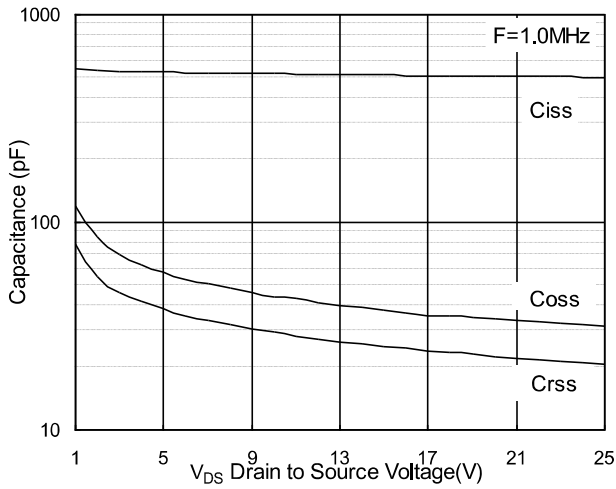


Fig.7 Capacitance

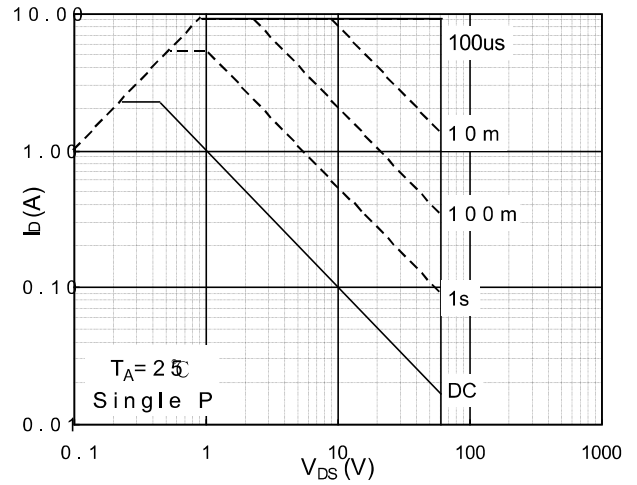


Fig.8 Safe Operating Area

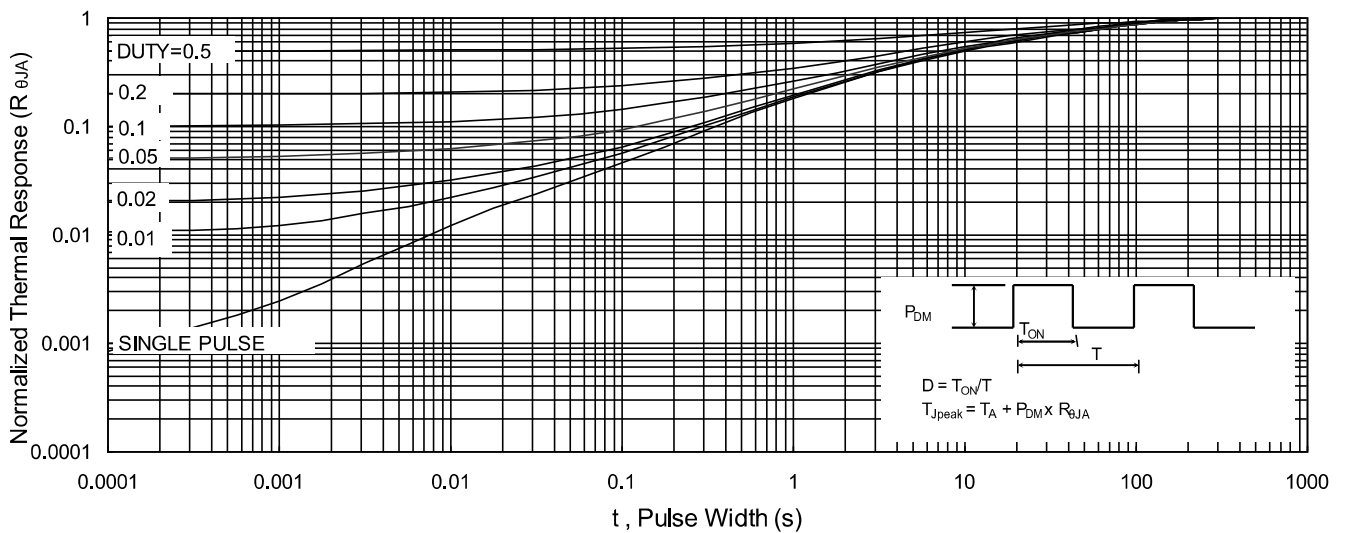


Fig.9 Normalized Maximum Transient Thermal Impedance

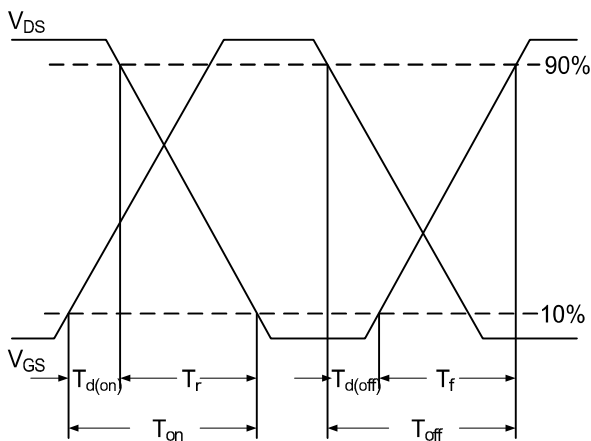


Fig.10 Switching Time Waveform

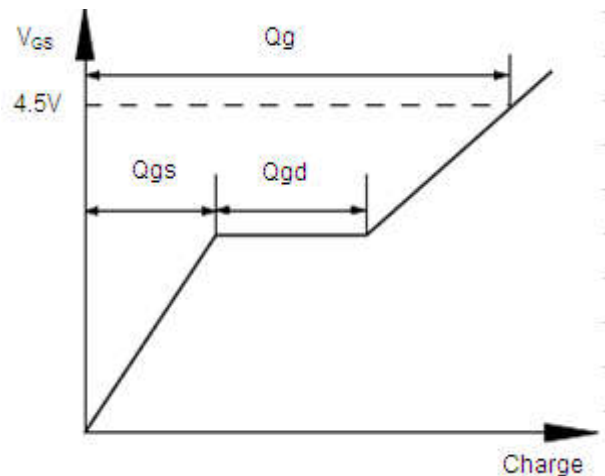


Fig.11 Gate Charge Waveform

Ordering information

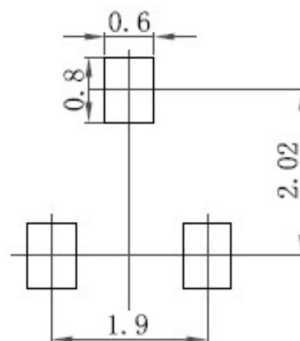
Package	Packing Description	Base Quantity	Packing Quantity
SOT-23	Tape/Reel,7"reel	3000pcs/Reel	24000PCS/Box 120000PCS/Carton

Package Dimensions

SOT-23

Dim.	Millimeter (mm)		mil	
	Min.	Max.	Min.	Max.
A	0.9	1.15	35	45
A1	0.1		3.9	
bp	0.38	0.48	15	19
C	0.09	0.15	3.54	5.9
D	2.8	3.0	110	118
E	1.2	1.4	47	55
E	1.9		75	
E1	0.95		37	
HE	2.1	2.55	83	100
Lp	0.15	0.45	5.9	18
Q	0.45	0.55	18	22
v	0.2		7.9	
W	0.1		4	

The recommended mounting pad size



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