

Transient Voltage Suppressor

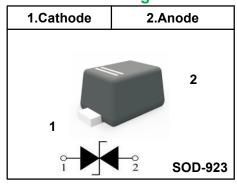
Features

- ♦80Watts peak pulse power tp =8/20us)
- **♦**Bidirectional configurations
- ♦Solid-state silicon-avalanchetechnology
- **♦**Low clamping volta
- ♦Low leakage current
- ♦IEC 61000-4-2 ±30KV contact ±30KV Air
- ♦IEC 61000-4-4 (EFT) 40A (5/50us)
- ♦IEC 61000-4-5 (Lightning 8/20µs): 8A

Application

- ♦Microprocessor based equipm
- ♦Personal Digital Assistants (PDA'S)
- Notebooks, Desktops, and Servers
- **♦Portable Instrumentat**
- ♦Pagers Peripher

Pinning



Marking Code		
ESD5VFBD923	С	

Limiting Values(TA = 25 °C, unless otherwise specified)

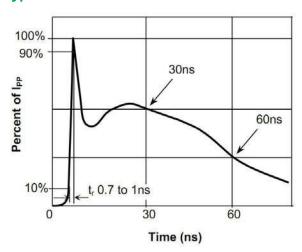
Symbol	Parameter	Conditions	value	Unit
V _{ESD}	Electrostatic Discharge Voltage	IEC 61000-4-2; Contact Discharge	±30	kV
		IEC 61000-4-2; Air Discharge	±30	kV
P _{PP}	Peak Pulse Power	tP = 8/20 μs	80	W
I _{PPM}	Rated Peak Pulse Current	tP = 8/20 μs	8	Α
TL	Lead Soldering Temperature	-	260(10seconds)	$^{\circ}$
TJ	Junction Temperature	-	-55 to+150	$^{\circ}$
Tstg	Storage Temperature Range	nge55 to+150		$^{\circ}$

Electrical Characteristics(TA = 25 °C unless otherwise specified

Symbol	Parameter	Conditions	Min	Тур.	Max	Unit
V _{RWM}	Reverse Working Voltage	TA = 25 °C	-	-	5.0	٧
V _{BR}	Breakdown Voltage	IT = 1mA	5.6	-	-	V
I _R	Reverse Leakage Current	VRWM = 5 V; TA = 25 °C	-	-	100	nA
Vc	Clamping Voltage	IPP=8A, tP =8/20μs	-	-	10	V
CJ	Junction Capacitance	VR = 0V, f = 1 MHz	-	15	-	pF



Typical Characteristics



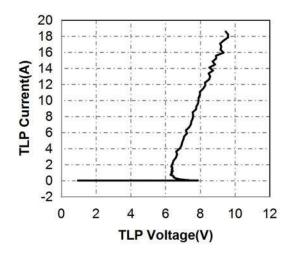


Fig.1 Pulse Waveform-ESD (IEC61000-4-2)

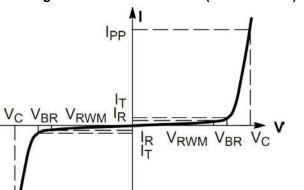


Fig.2 Transmission Line Pulse (TLP)

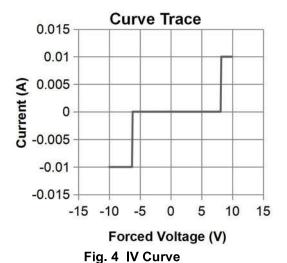
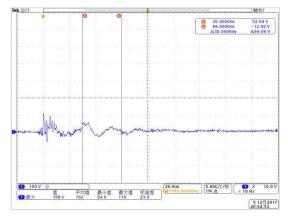


Fig.3 V-I Capacitance For Bidirectional Diode

IPP



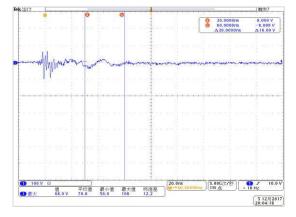


Fig.5 Clamping Voltage at IEC61000-4-2 +8kV Pulse Waveform

Fig.6 Clamping Voltage at IEC61000-4-2
-8kV Pulse Waveform

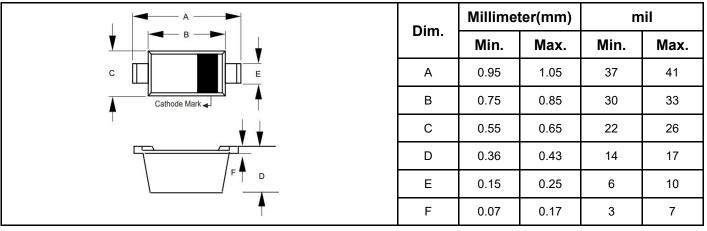


Ordering information

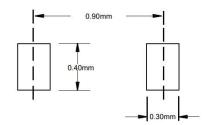
Package	Packing Description	Packing Quantity
SOD-923	Tape/Reel,7"reel	8000PCS/Reel 320000PCS/Carton

Package Dimensions

SOD-923



The recommended mounting pad size





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